

# Advanced Mobile Devices Analysis Using JTAG and Chip-off

Forensics Ninja



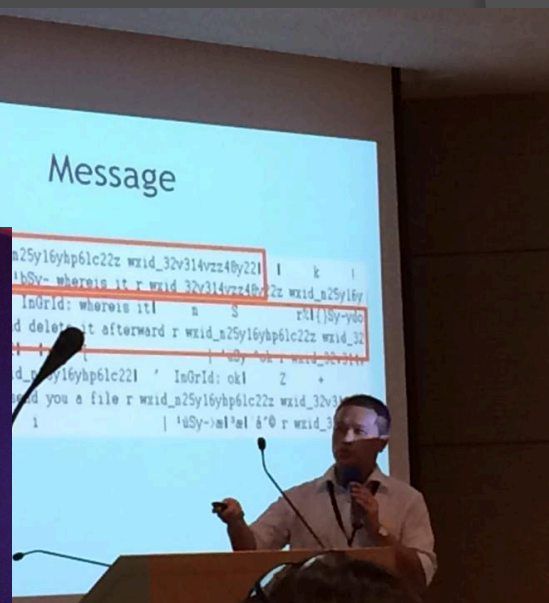
# Who am I

- Captain a.k.a Forensics Ninja
- Research since 2010
- Facebook Forensics (2011) on Hakin9 Magazine
- Mac Memory Forensics (2014) on Digital Forensics Magazine
- Investigation and Intelligence Framework (2015) on Forensics Focus
- Advanced Mobile Devices Analysis Using JTAG and Chip-Off (2016)



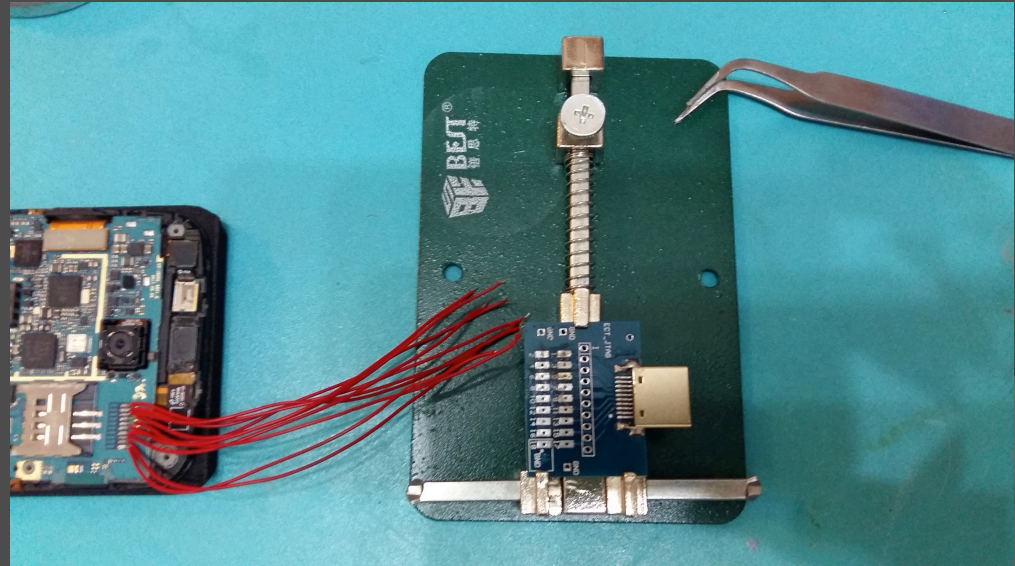
# Speaker@

- SANS DFIR
- DefCON 20
- HITCON
- AVTokyo
- APWG
- HTCIA
- VXCON



# Agenda

- Introduction to JTAG and Chip Off
- Analysis on JTAG and eMMC dump
- Demo



# Training Course

- TeelTech Advanced JTAG / Chip-Off Mobile Forensics
- Cellebrite JTAG Extraction and Decoding
- H11 JTAG Data Recovery and Mobile Phone Repair
- XRY Advanced Acquisition Training
- viaforensics (NowSecure) until 2012
- Course Fee around USD4,000

# Why JTAG / Chip-Off?

- Physical vs Logical vs Forensics tools
- Bricked
- Locked without debugging mode
- Damaged
- Special cases

# What is JTAG?

- Joint Test Action Group
- Test Access Ports (TAPs) to collect raw data from a memory chips
- Not chip-off and ISP
- Extreme physical data acquisition
- Advanced technique
- Soldering and De-soldering



# Price lists

- Source from NowSecure
- US dollars



Soldering iron and stand \$50

Solder and solder wick \$15

Kynar wire \$15

Magnifying light \$65

Power supply \$100

Tool Kit \$35

Multimeter tool \$40

Jumper wires \$45

PC Header \$5

RIFF Box JTAG \$170

Medusa JTAG \$160

Optional adapters \$80

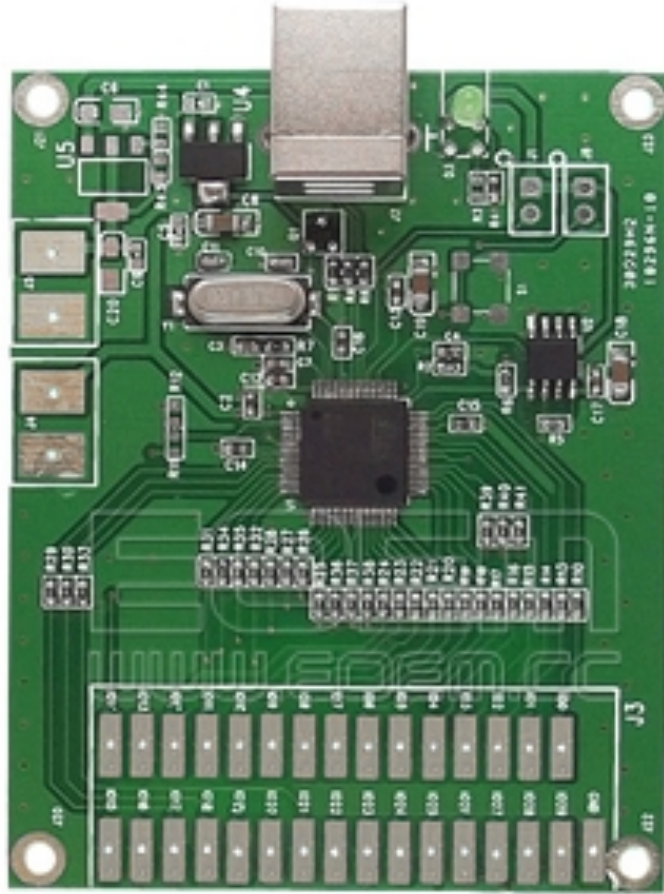
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Total: **\$780**



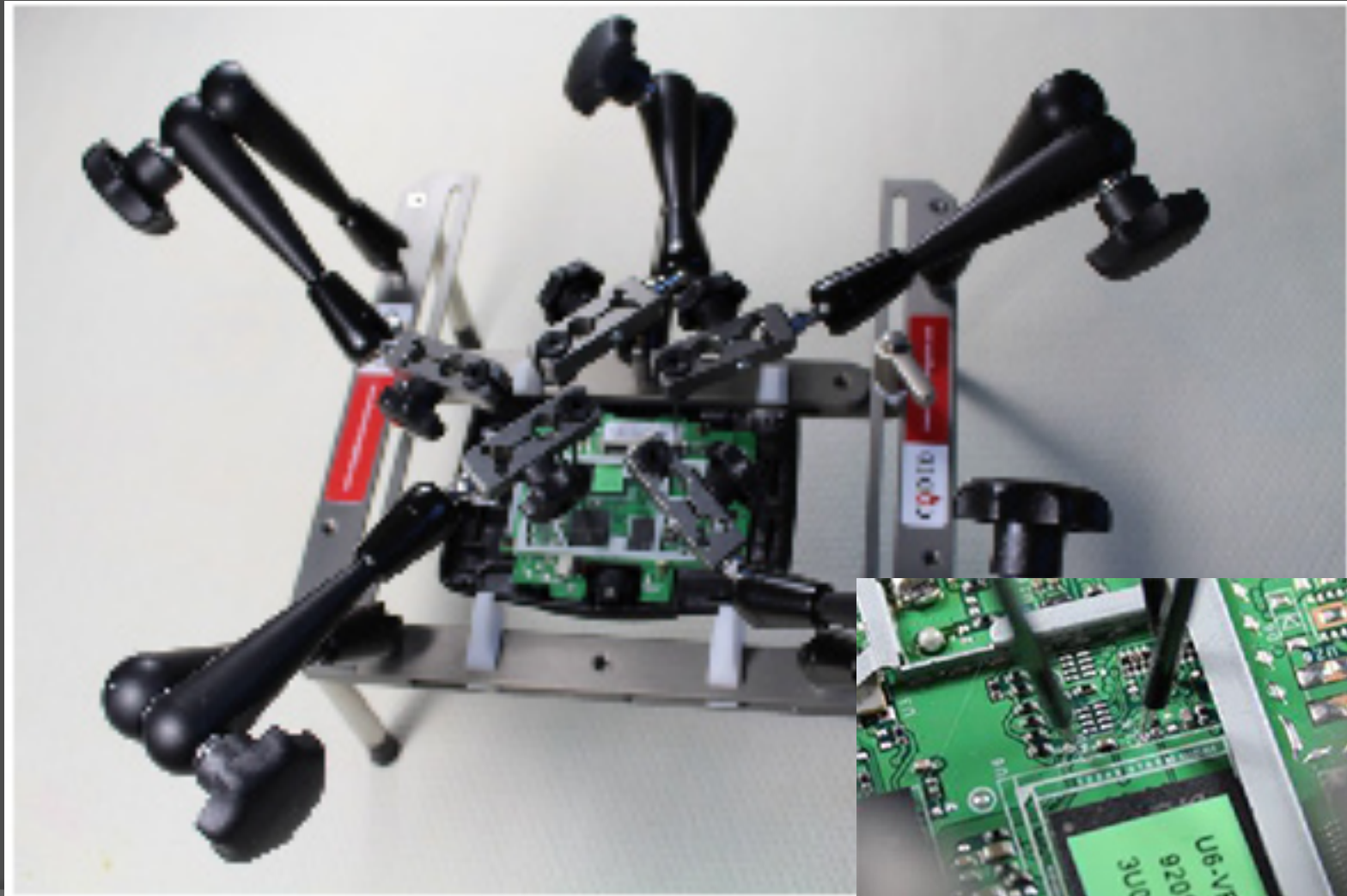
# JTAG Box



# JTAG Finder



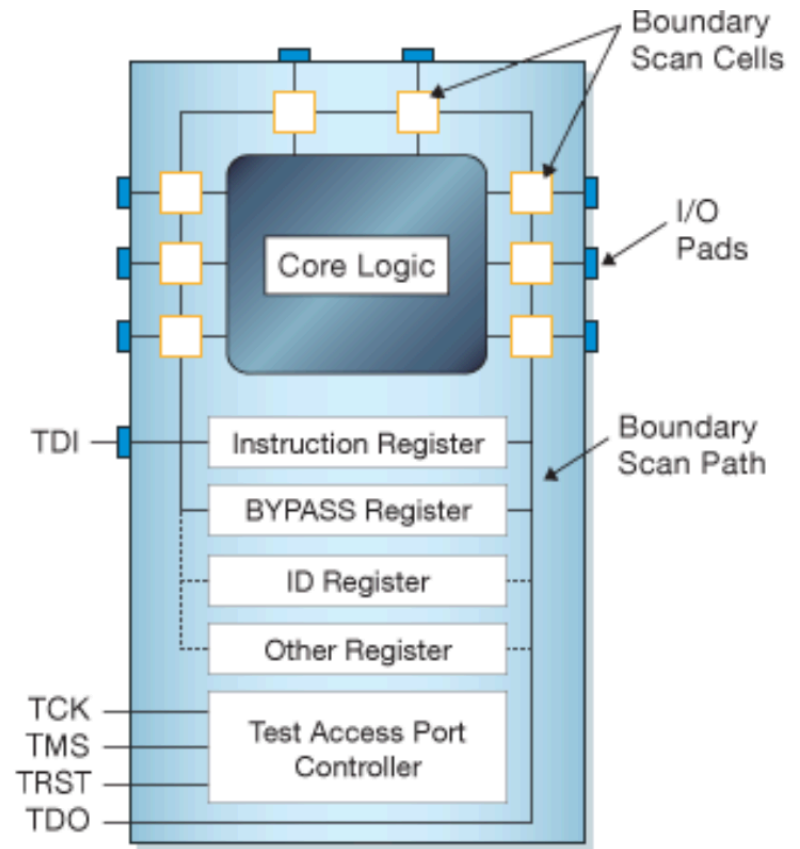
# Mounting Frame & Arms



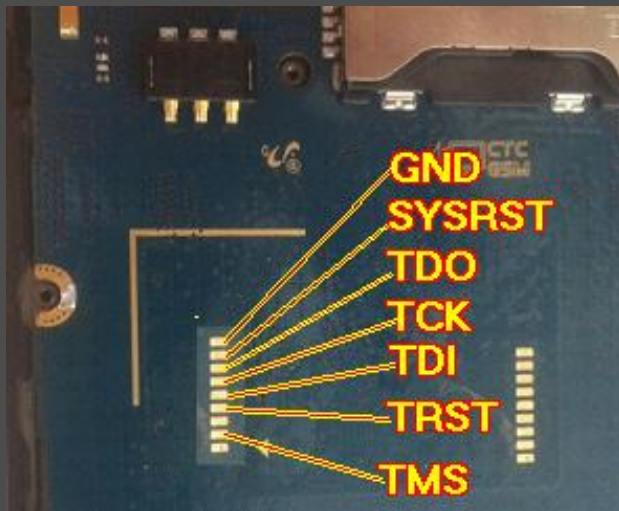
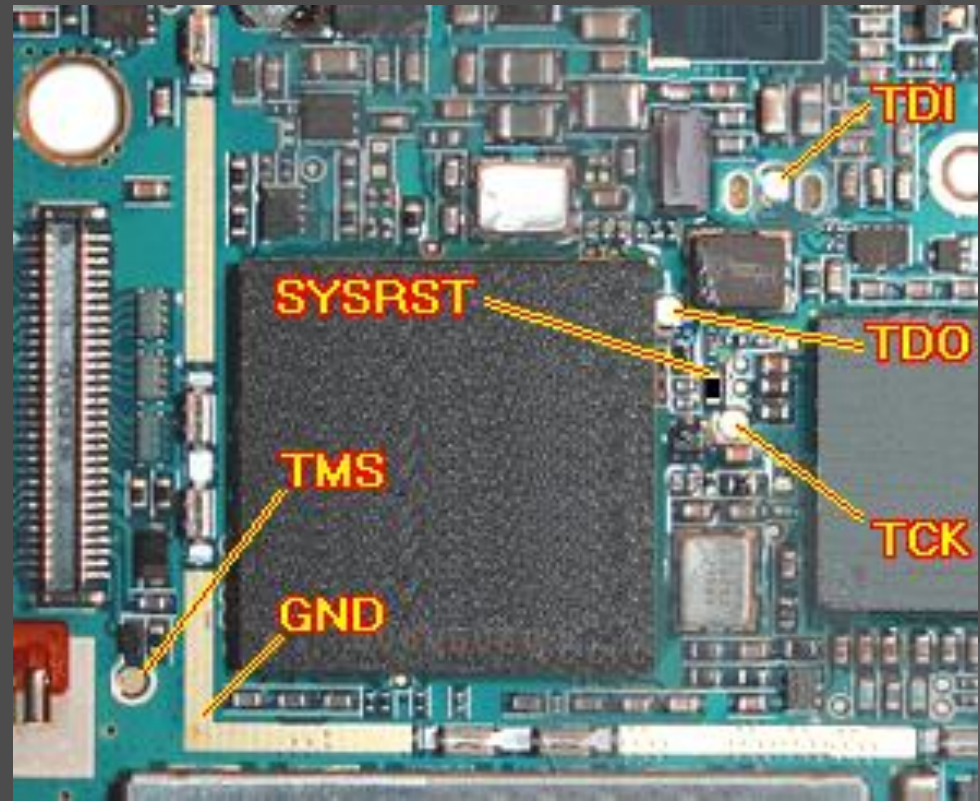
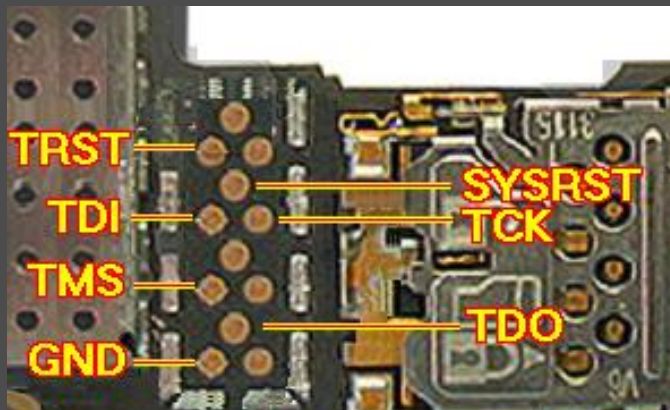


# TAP

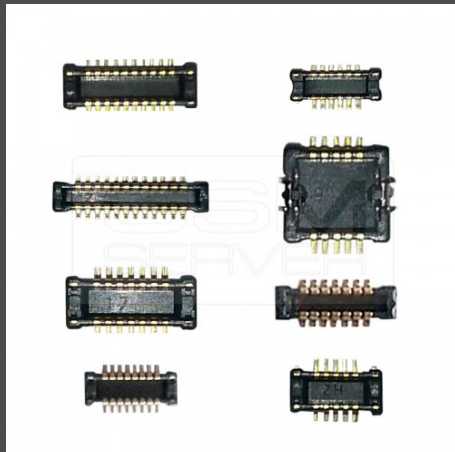
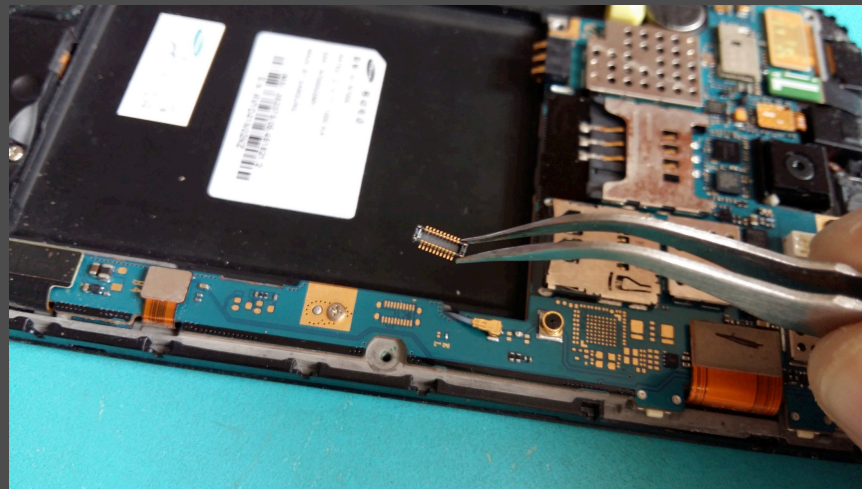
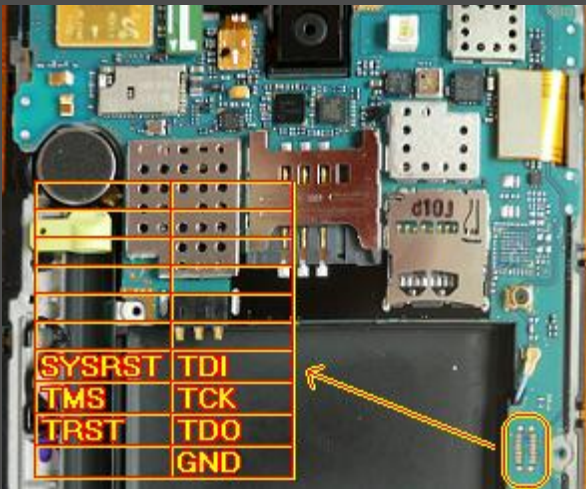
- ⦿ TCK – test clock
- ⦿ TMS – test mode state
- ⦿ TDI – test data in
- ⦿ TDO – test data out
- ⦿ TRST – test reset
- ⦿ NRST – normal reset
- ⦿ RTCK – return clock
- ⦿ GND – ground



# JTAG Pinout

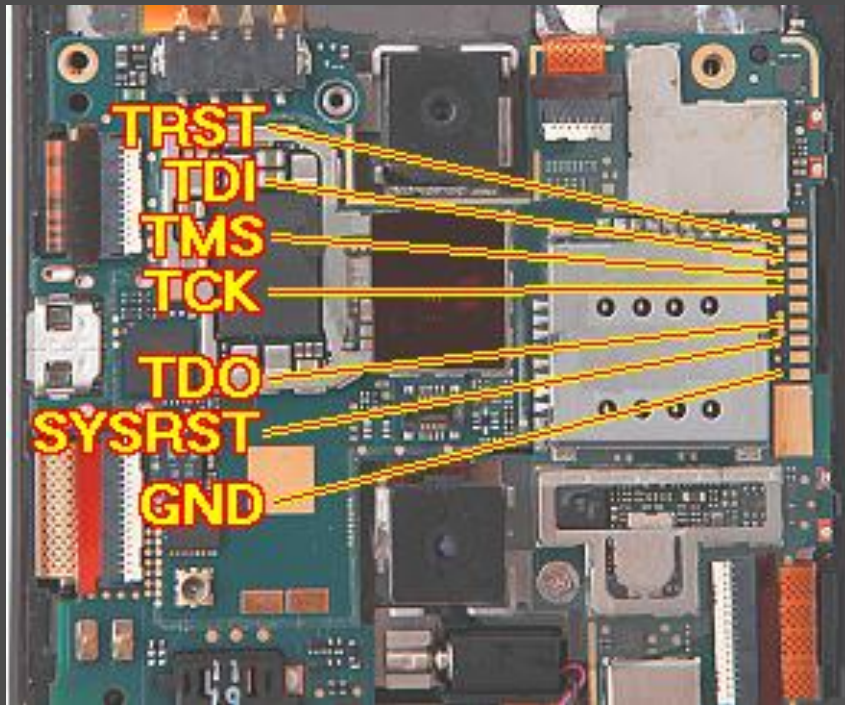


# JTAG Molex and Jig



# Demonstration using Riff Box

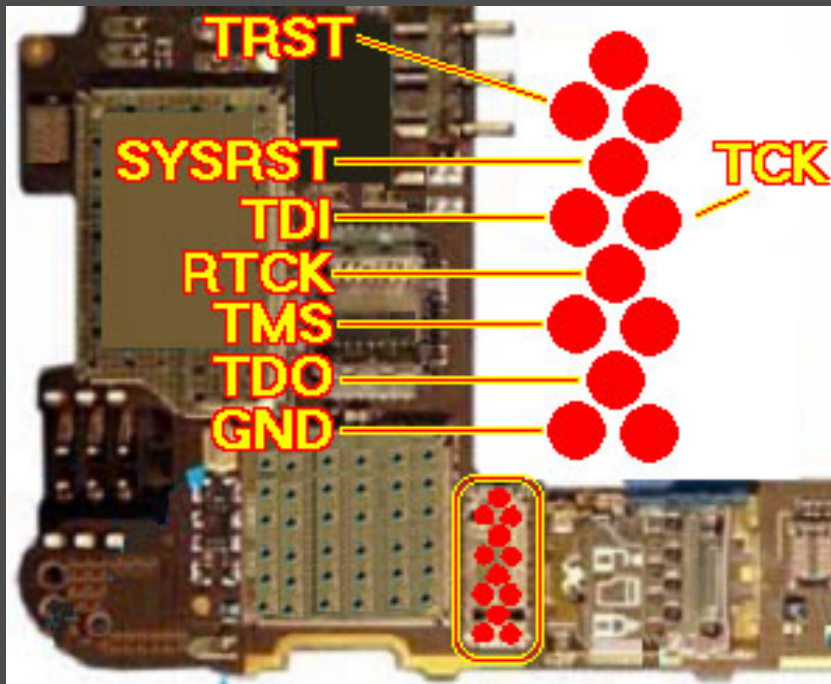
- HTC EVO 3G
- Android OS

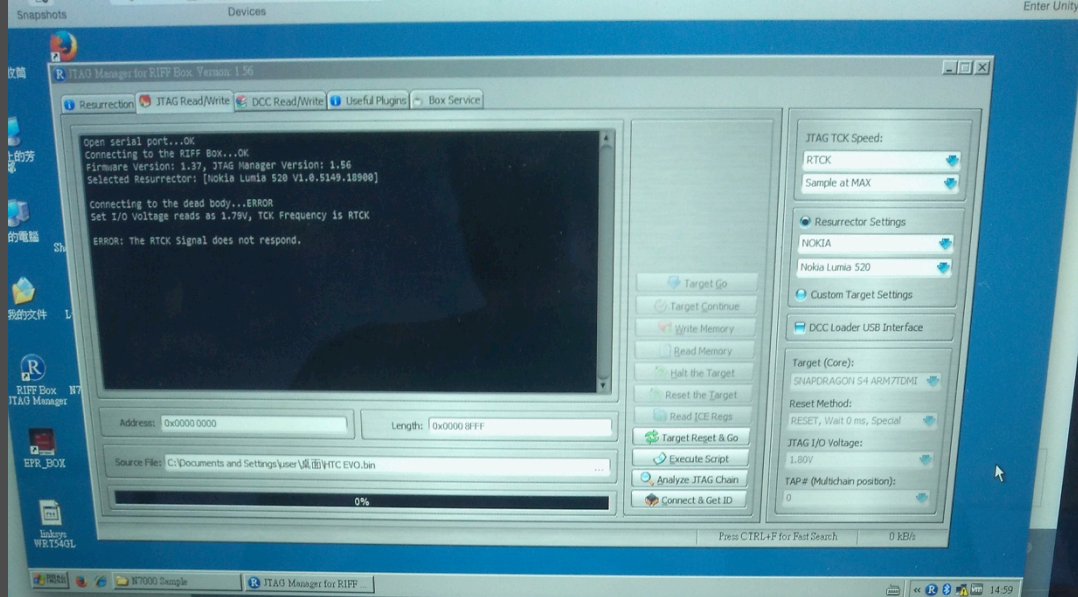




# Demonstration using Riff Box

- Lumia 620
- Windows 8 OS





# Acquisition of the Flash Rom

Detected dead body ID: 0x104210E1 - CORRECT!  
Set I/O Voltage reads as 1.79V, TCK Frequency is RTCK  
Adaptive Clocking RTCK Sampling is: [Sample at MAX]  
Settings Code: 0x0001000000000000000000000020000000

Resurrection sequence started.  
Establish communication with the phone...OK  
Initializing internal hardware configuration...OK  
Uploading resurrector data into memory...OK  
Starting communication with resurrector...OK

Detected an Initialized FLASH1 Chip, ID: 0x0090/0x004A (HYNIX, 0x000090000000 Bytes = 2.25 GB)

Reading FLASH1 address space from 0x000000000000 to 0x00001FFFFFFF

JTAG TCK Speed:

RTCK

Sample at MAX

Resurrector Settings

HTC

HTC EVO3D (PG8630000)

Custom Target Settings

Resurrection sequence started.  
Establish communication with the phone...OK  
Initializing internal hardware configuration...OK  
Uploading resurrector data into memory...OK  
Starting communication with resurrector...OK

Detected an Initialized FLASH1 Chip, ID: 0x0090/0x014A (H8G2D, 0x0001CE800000 Bytes = 7.23 GB)

Detected an Initialized FLASH2 Chip, ID: 0x0090/0x014A (H8G2D, 0x000000200000 Bytes = 2.00 MB)

Looking for the RIFF BOX DCCLoader USB Interface Port...FAILED  
The DCCLoader USB Interface is not found. Communication continues over the DCC Interface

Reading FLASH1 address space from 0x000000000000 to 0x0001CE7FFFFFFF

JTAG TCK Speed:

RTCK

Sample at 200 kHz

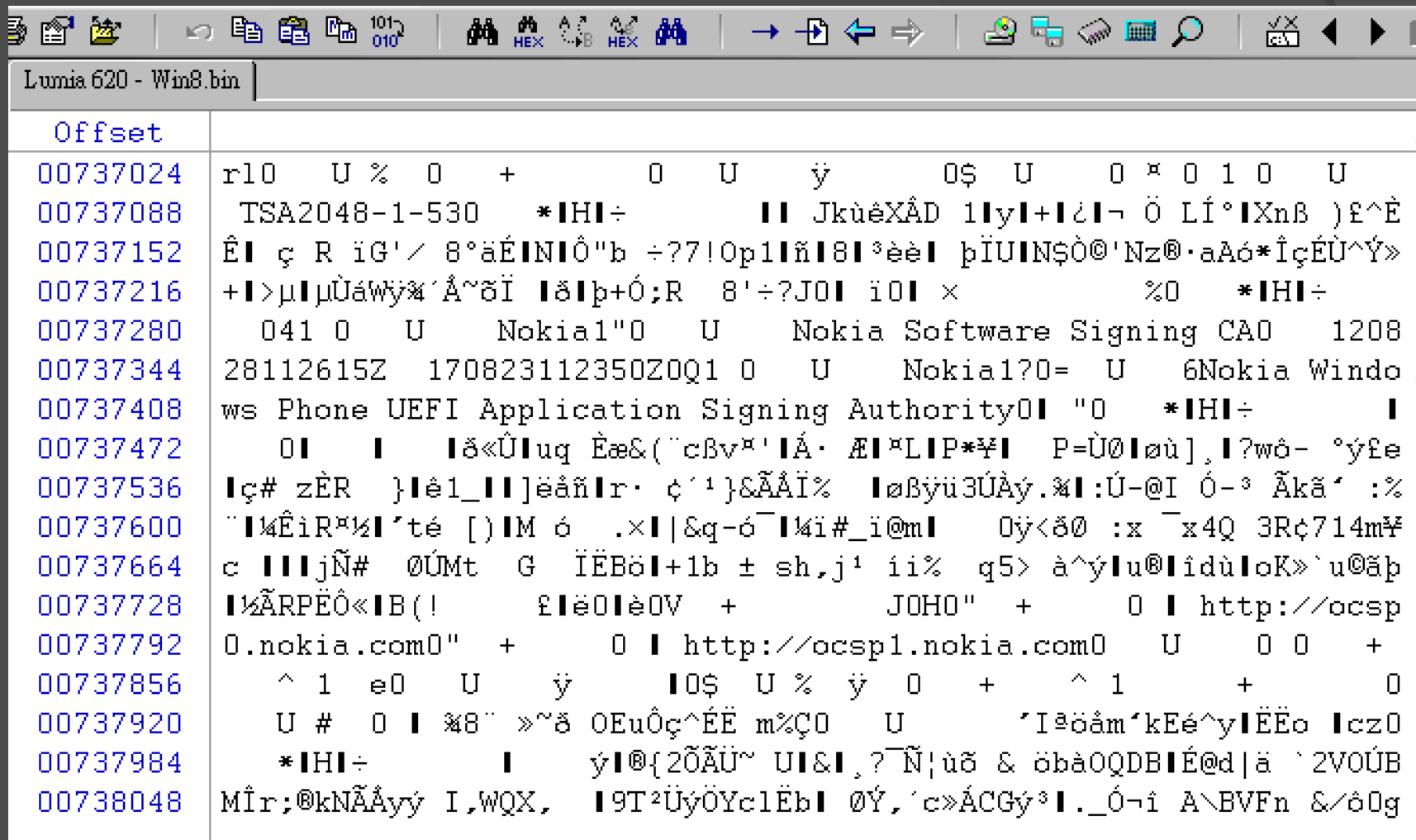
Resurrector Settings

NOKIA

Nokia Lumia 620

Custom Target Settings

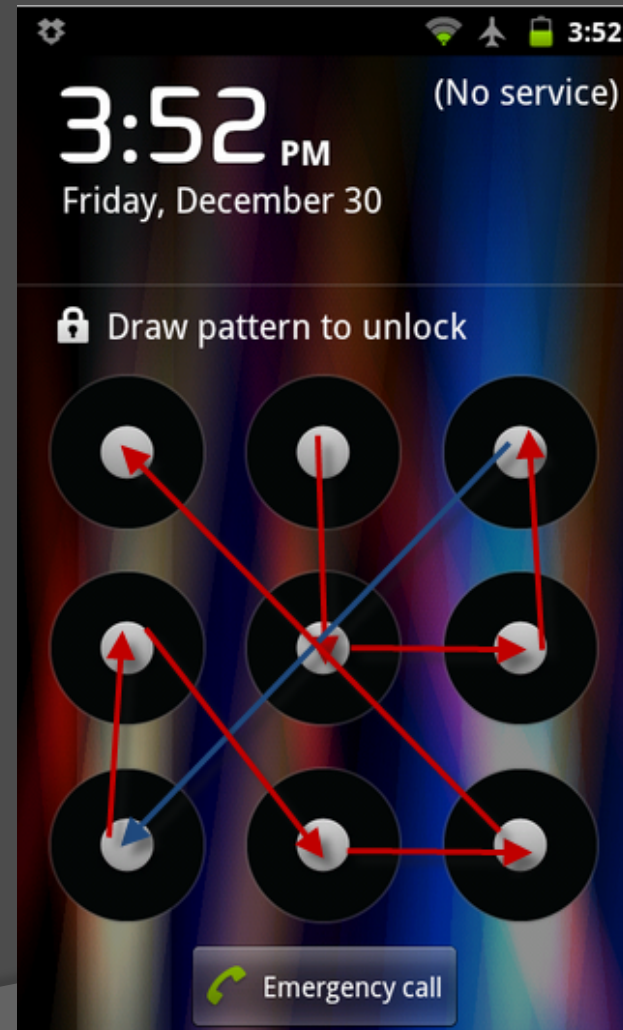
# Retrieved Data from phone



Offset	Data
00737024	r10 U % 0 + 0 U ý 0\$ U 0 * 0 1 0 U
00737088	TSA2048-1-530 * H ÷    JkùèXÂD 1 y + ç - Ö Lí° XnB )É^È
00737152	Ê  ç R iG' / 8°äÉ N Ô"b ÷??!Op1 ñ 8 °èè  p U N\$Ô@'Nz@·aAó*ÎçÉÛ^Ý»
00737216	+ >µ µÛáwý*´Á~öÏ  ä p+Ó;R 8'÷?J0  i0  × %0 * H ÷
00737280	041 0 U Nokia1"0 U Nokia Software Signing CAO 1208
00737344	28112615Z 170823112350Z0Q1 0 U Nokia1?0= U 6Nokia Windo
00737408	ws Phone EFI Application Signing Authority0  "0 * H ÷
00737472	0     ä«Û uq Èæ&("cBv"  Á· Æ P P*¥  P=Û0 øù], ?wô- °ýfe
00737536	ç# zÈR } èl_ ] èãñ r· ç'¹} &ÃÃ %  øByü3UÀý.* :Ú-@I Ó-³ Äkã' :%
00737600	" kÊiR"½ 'té [ ]M ó .x   &q-ó- ki#_i@m  0ý<ð0 :x x4Q 3Rç714m¥
00737664	c    jÑ# 0ÛMt G  ÈBö +1b ± sh,j¹ ii% q5> à^ý u@ ídù oK»`u@äp
00737728	kÄRPEÔ« B(!  è0 è0V + JOHO" + 0   http://ocsp
00737792	0.nokia.com0" + 0   http://ocsp1.nokia.com0 U 0 0 +
00737856	^ 1 e0 U ý  0\$ U % ý 0 + ^ 1 + 0
00737920	U # 0   %8" »~ä 0EuÔç^ÉË m%Ç0 U 'Iäöám´kEé^y ÈËo  cz0
00737984	* H ÷   ý @{2ÖÃÛ~ U & ,?Ñ ;ùö & öbàOQDB É@d ä `2VOÛB
00738048	Mîr;@kNÄÄýý I,WQX,  9T²ÛýÖYc1Èb  ØÝ,'c»ÁCGý³ ._Ó-í A\BVFñ &/ð0g

# Decoding the Lock Pattern

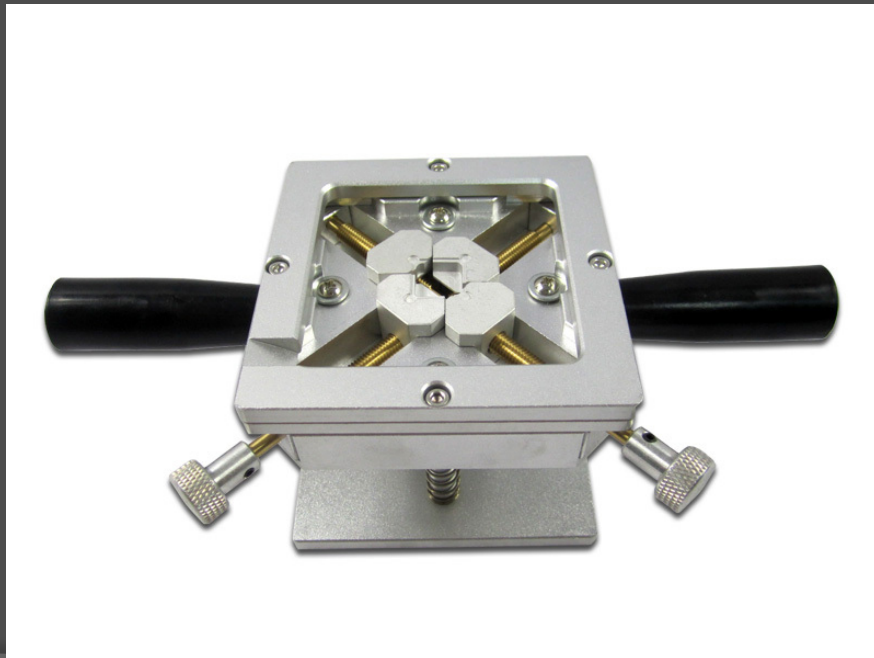
- gesture.key
- 20 bytes in length
- open source tools





# What is Chip-Off?

- ⦿ eMMC chip
- ⦿ NAND Flash
- ⦿ Disassemble & Re-balling

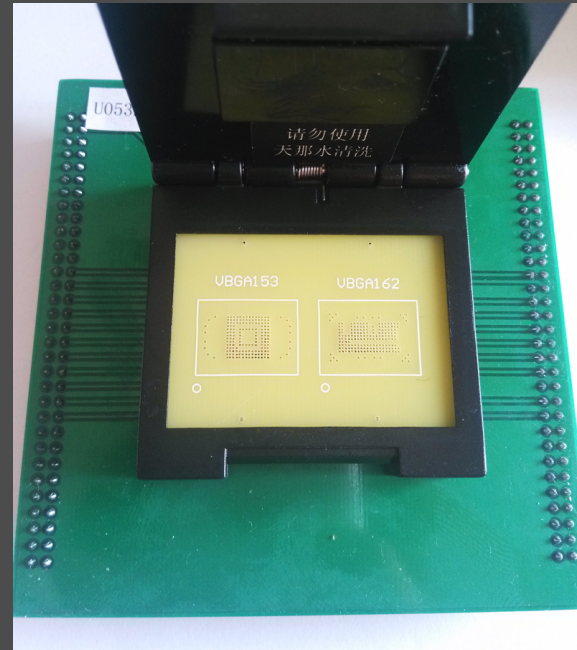


# Heating Machine / Stand





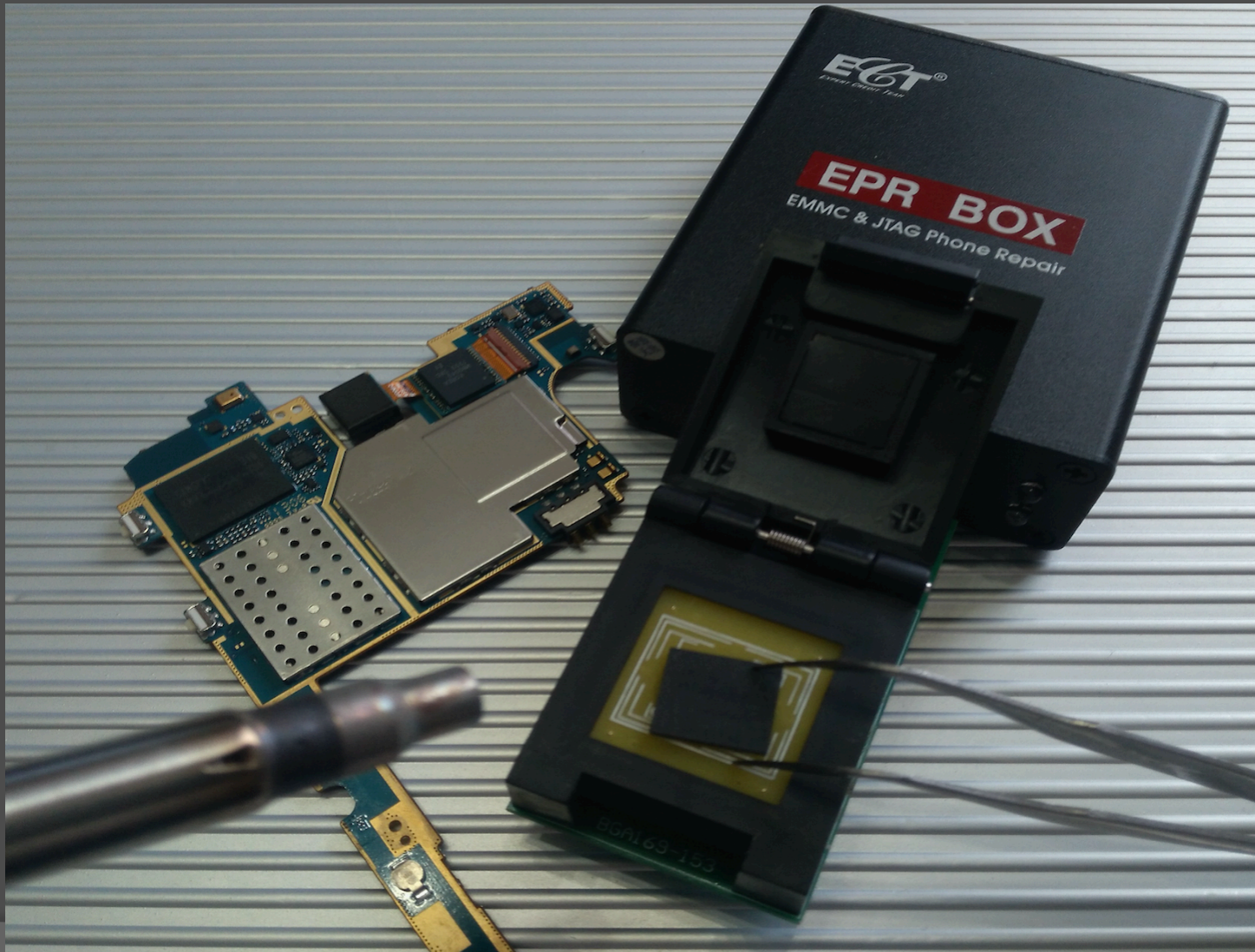
# eMMC Programmer & Adapters



# eMMC Box

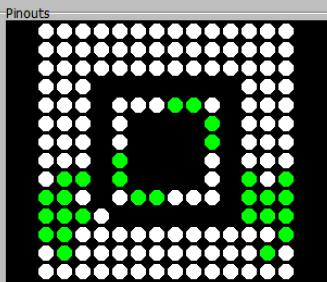


# EPR Box & BGA 169e adaptor



# Demonstration

Loose contact  
 Short circuit  
 Good contact



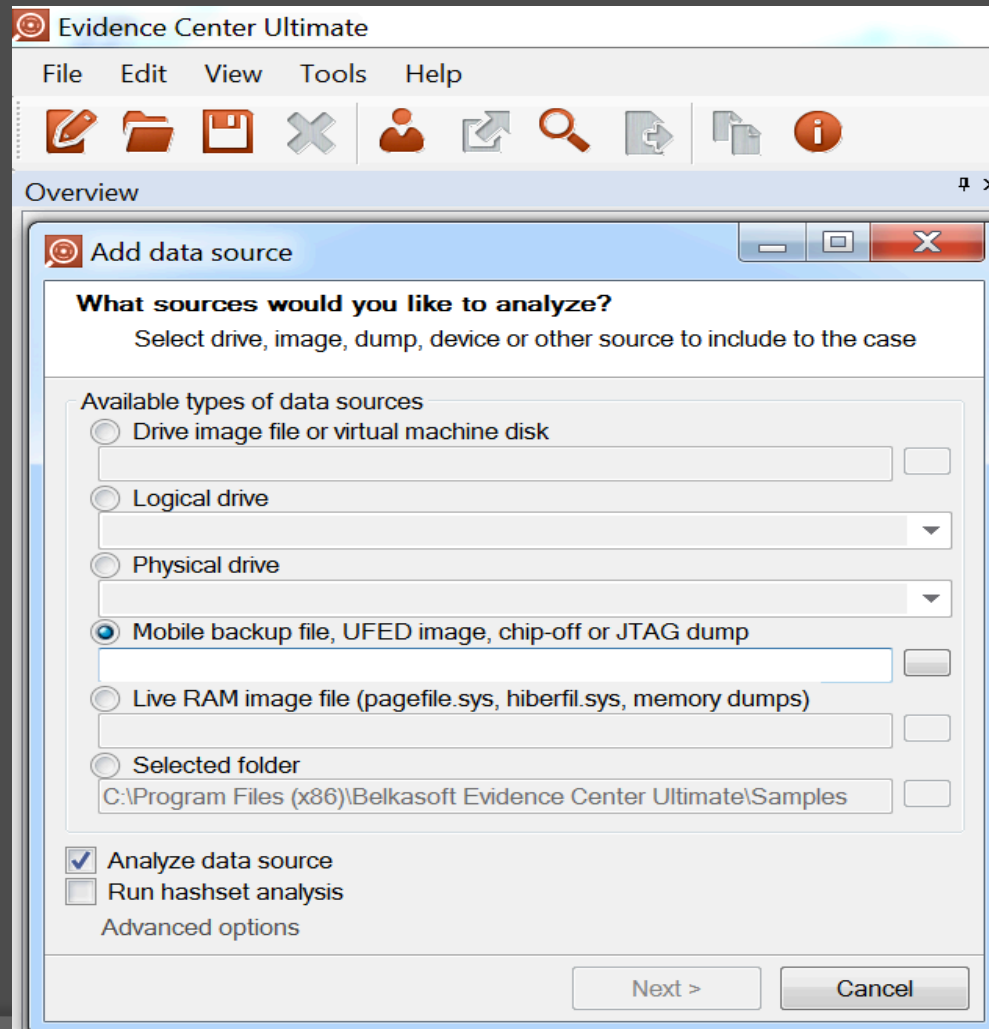
```

SN:00000803119
Version:V3.1
Device Ready...OK!
Read Info...
Pin Check up...
Pin Check up...OK!
Device Ready...
Device Ready...OK!
ID check...
ID check: 00010015
Product name: VYL00M
User partition size: 3A8000000 ( 15024M / 14.671G),
15753805824 Bytes
Boot partition size: 0x200000 ( 2.0M )
RPMB partition size: 0x20000 ( 0.1M )
ID check...OK!
ID check success. time:0.80 sec.
Reading...
Device Ready...
Device Ready...OK!
    
```

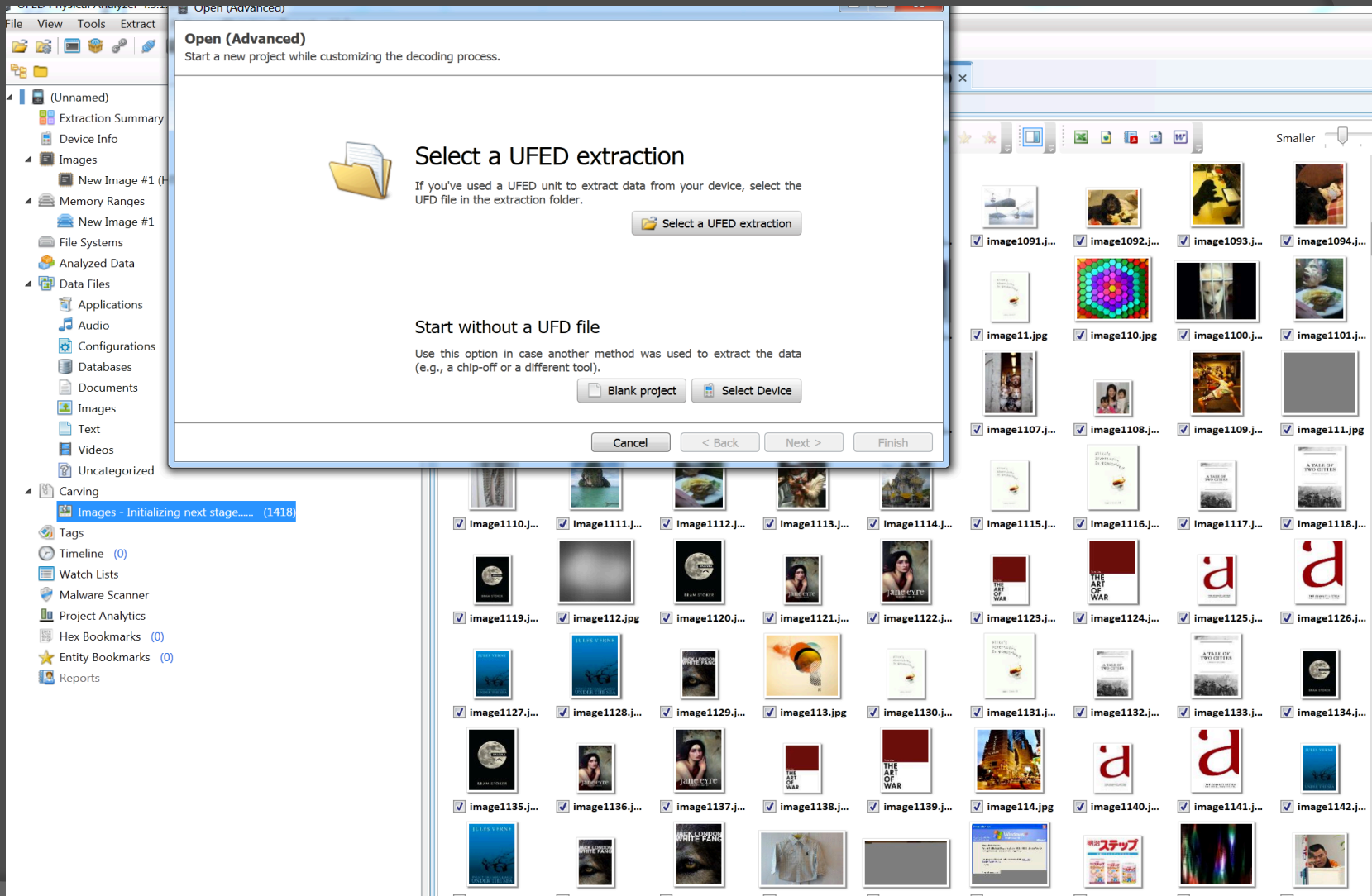
User : C:\东海EPR\_BOX v3.183\emmc\emmc.bin  
 Boot1: C:\东海EPR\_BOX v3.183\emmc\emmc.boot1  
 Boot2: C:\东海EPR\_BOX v3.183\emmc\emmc.boot2  
 Ext\_CSD : C:\东海EPR\_BOX v3.183\emmc\emmc.ext\_csd  
 RPMB: C:\东海EPR\_BOX v3.183\emmc\emmc.rpmb

Value	Name	Field	Width	Cell Type	CSD-slice
<input type="checkbox"/> 0x03	CSD structure	CSD_STRUCTURE	2	R	[127:126]
<input type="checkbox"/> 0x04	System specification version	SPEC_VERS	4	R	[125:122]
<input type="checkbox"/> -	Reserved	-	2	R	[121:120]
<input type="checkbox"/> 0x27	Data read access-time 1	TAAC	8	R	[119:112]
<input type="checkbox"/> 0x01	Data read access-time 2 in CLK cycles (NSAC*100)	NSAC	8	R	[111:104]
<input type="checkbox"/> 0x32	Max. bus clock frequency	TRAN_SPEED	8	R	[103:96]
<input type="checkbox"/> 0xF5	Device command classes	CCC	12	R	[95:84]
<input type="checkbox"/> 0x09	Max. read data block length	READ_BL_LEN	4	R	[83:80]
<input type="checkbox"/> 0x00	Partial blocks for read allowed	READ_BL_PARTIAL	1	R	[79:79]
<input type="checkbox"/> 0x00	Write block misalignment	WRITE_BLK_MISALIGN	1	R	[78:78]
<input type="checkbox"/> 0x00	Read block misalignment	READ_BLK_MISALIGN	1	R	[77:77]
<input type="checkbox"/> 0x00	DSR implemented	DSR_IMP	1	R	[76:76]
<input type="checkbox"/> -	Reserved	-	2	R	[75:74]
<input type="checkbox"/> 0xFFF	Device size	C_SIZE	12	R	[73:62]
<input type="checkbox"/> 0x06	Max. read current @ VDD min	VDD_R_CURR_MIN	3	R	[61:59]
<input type="checkbox"/> 0x06	Max. read current @ VDD max	VDD_R_CURR_MAX	3	R	[58:56]
<input type="checkbox"/> 0x06	Max. write current @ VDD min	VDD_W_CURR_MIN	3	R	[55:53]
<input type="checkbox"/> 0x06	Max. write current @ VDD max	VDD_W_CURR_MAX	3	R	[52:50]
<input type="checkbox"/> 0x07	Device size multiplier	C_SIZE_MULT	3	R	[49:47]
<input type="checkbox"/> 0x1F	Erase group size	ERASE_GRP_SIZE	5	R	[46:42]
<input type="checkbox"/> 0x1F	Erase group size multiplier	ERASE_GRP_MULT	5	R	[41:37]
<input type="checkbox"/> 0x1F	Write protect group size	WP_GRP_SIZE	5	R	[36:32]
<input type="checkbox"/> 0x01	Write protect group enable	WP_GRP_ENABLE	1	R	[31:31]
<input type="checkbox"/> 0x00	Manufacturer default ECC	DEFAULT_ECC	2	R	[30:29]

# Belkasoft Evidence Center



# UFED Physical Analyzer



# HITCON Training (Wish)

- November 2016
- One Day (6 hours)

Question?

[forensicsninja@gmail.com](mailto:forensicsninja@gmail.com)